
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: RAMBALDI et al.

Atty Dkt No.: SGSTP009D1/97-B-068D1

Application No.: 10/613,830

Examiner: Hannett, J.

Filed: July 3, 2003

Group: 2622

Title: PIXEL CORRECTION SYSTEM AND
METHOD FOR CMOS IMAGERS

Confirmation No: 6799

CERTIFICATE OF EFS-WEB TRANSMISSION

I hereby certify that this correspondence is being transmitted electronically through EFS-WEB to the Commissioner for Patents, P.O. Box 1450 Alexandria, VA 22313-1450 on April 23, 2007.

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Sir:

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Respectfully submitted,
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